

RESPONSE UNDER 37 CFR 1.116
EXPEDITED PROCEDURE EXAMINING
GROUP 2811

201540US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: :
TOMOHIRO YAMASHITA ET AL. : GROUP ART UNIT: 2811
SERIAL NO.: 09/754,325 :
FILED: JANUARY 5, 2001 : EXAMINER: NADAV, O.
FOR: SEMICONDUCTOR DEVICE :

AMENDMENT

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

In response to the Official Action dated December 10, 2002, please amend the above-identified patent application as follows:

IN THE CLAIMS

Please amend Claims 1 and 13 as shown in the attached marked-up copy to read as follows:

1. (Twice Amended) A semiconductor device comprising:
a semiconductor substrate;
a first well of a prescribed conductivity type at which a first active element is provided, said first well being selectively formed in a surface of said semiconductor substrate;
a second well of the same conductivity type as said prescribed conductivity type at which a second active element is provided, said second well being selectively formed in said surface of said semiconductor substrate;

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